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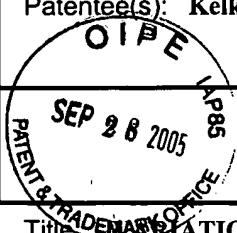
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TRANSMITTAL LETTER  
(General - Patent Issued)

Docket No.

BUR920030134US1

Patentee(s): Kelkar et al.



U.S. Patent No.

6,940,324 B2

Issue Date

September 6, 2005

Title: ~~TRADEMARK~~ VARIATION OF EFFECTIVE FILTER CAPACITANCE IN PHASE LOCK LOOP CIRCUIT LOOP  
FILTERSCOMMISSIONER FOR PATENTS:

Transmitted herewith is:

Certificate of Correction  
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No fee required below. Errors were made by the U.S. Patent and Trademark Office

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Dated: 09-26-2005

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## CERTIFICATE OF CORRECTION

PATENT NO. : 6,940,324 B2

DATED : September 6, 2005

INVENTOR(S) : Kelkar et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1

Line 47, delete "the first the first" and insert --the first--

Column 5

Line 18, delete " $C_{\text{eff}} = (C^*G_m)/(G_m = G_s)$ " and insert  $-C_{\text{eff}} = (C^*G_m)/(G_m + G_s)$  --

Column 6

Line 29 and Line 32, delete "the first the first" and insert --the first--

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PATENT NO. 6,940,324 B2

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